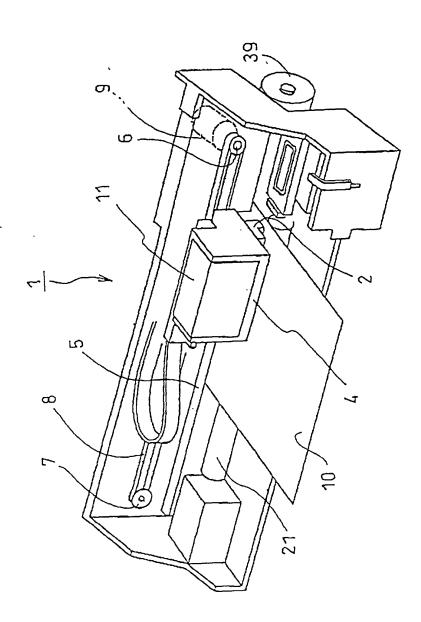
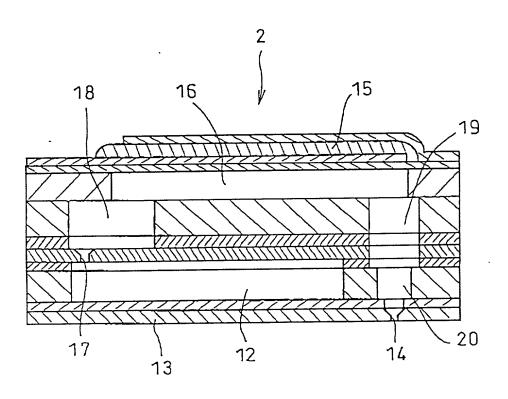
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Fig. 2

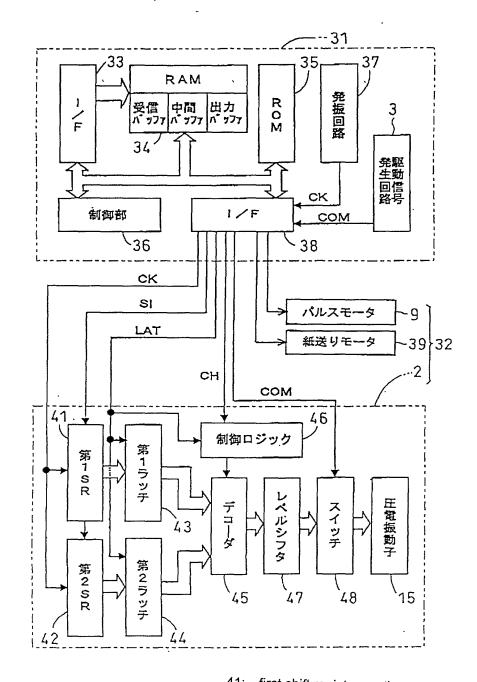


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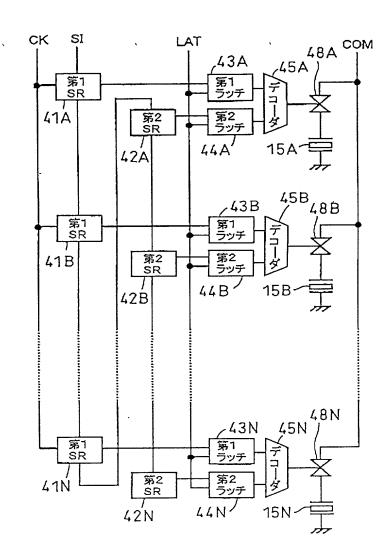


- drive signal generator 3:
- 9: pulse motor
- 15: piezoelectric vibrator
- 33: external I/F
- 34: reception buffer, intermediate buffer, output buffer
- 36: controller
- 37: oscillator
- 38: internal I/F
- 39: paper feeding motor

- 41: first shift register section
- second shift register section 42:
- 43: first latching section
- second latching section 44:
- 45: decoder
- 46: control logic
- 47: level shifter
- 48: switcher

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Fig. 4



41A ... 41N: first shift register element

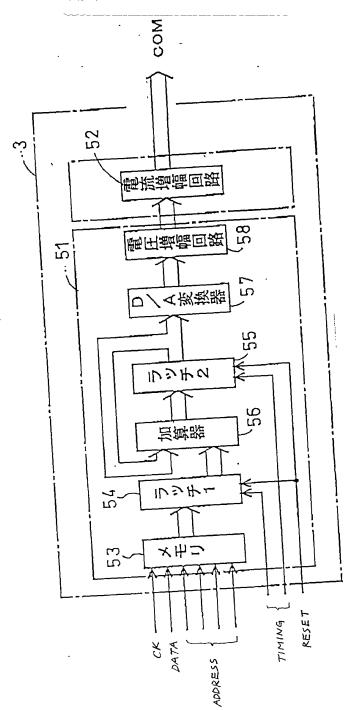
42A ... 42N: second shift register element

43A ... 43N: first latching element

44A ... 44N: second latching element

45A ... 45N: decoder element

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waveform memory current amplifier

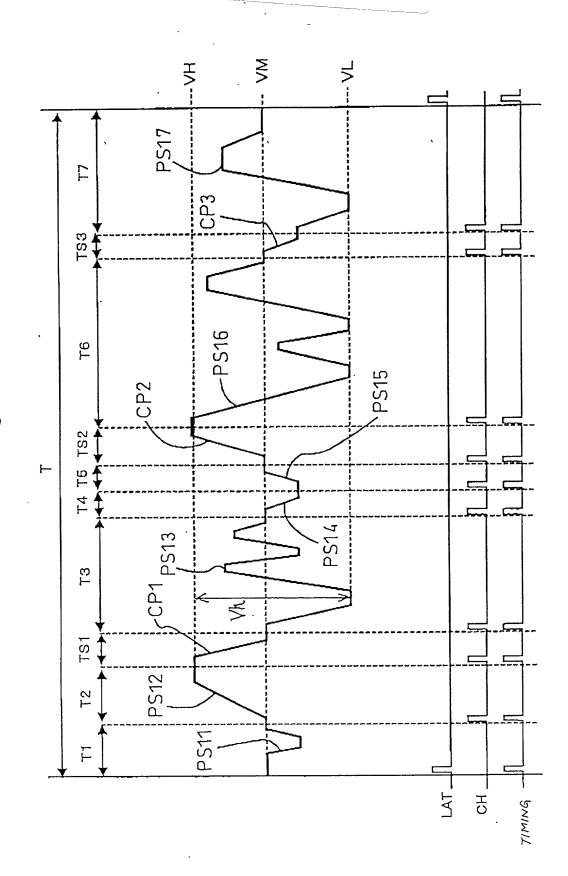
second waveform latching section first waveform latching section 54:

55:

D/A converter adder 57: 56:

voltage amplifier

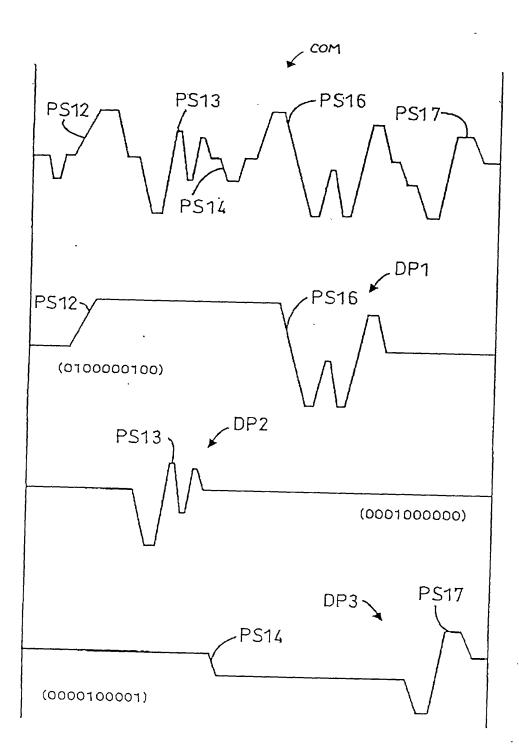
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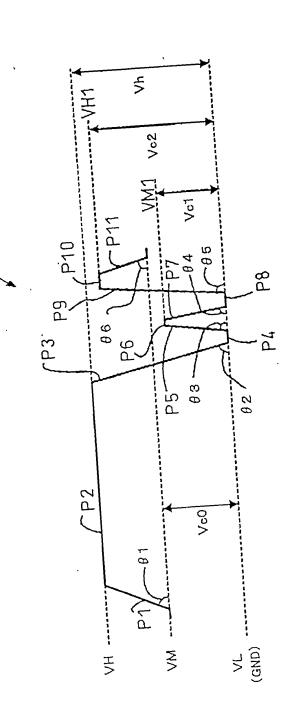
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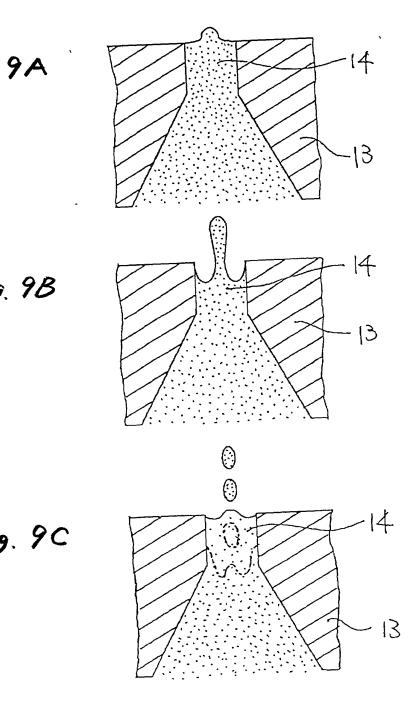
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